Document made available under the Patent Cooperation Treaty (PCT)

International application number: PCT/US05/001884

International filing date:

21 January 2005 (21.01.2005)

Document type:

Certified copy of priority document

Document details:

Country/Office: US

Number:

60/537,889

Filing date:

22 January 2004 (22.01.2004)

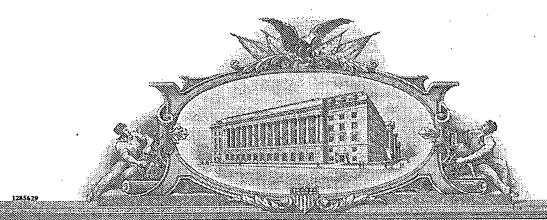
Date of receipt at the International Bureau: 21 February 2005 (21.02.2005)

Remark: Priority document submitted or transmitted to the International Bureau in

compliance with Rule 17.1(a) or (b)



World Intellectual Property Organization (WIPO) - Geneva, Switzerland Organisation Mondiale de la Propriété Intellectuelle (OMPI) - Genève, Suisse



SIMPON, METALLES, CERTAINS THE SECTION OF THE SECTI

UNITED STATES DEPAREMENT OF COMMERCE

United States Patent and Trademark Office

February 15, 2005

THIS IS TO CERTIFY THAT ANNEXED HERETO IS A TRUE COPY FROM THE RECORDS OF THE UNITED STATES PATENT AND TRADEMARK OFFICE OF THOSE PAPERS OF THE BELOW IDENTIFIED PATENT APPLICATION THAT MET THE REQUIREMENTS TO BE GRANTED A FILING DATE.

APPLICATION NUMBER: 60/537,889

FILING DATE: January 22, 2004 RELATED PCT APPLICATION NUMBER: PCT/US05/01884

Certified by

Under Secretary of Commerce for Intellectual Property and Director of the United States

Patent and Trademark Office

	6
Und	1

Please type a plus sign (+) inside this box ----

Revised PTO/SB/16 (8-00)
Approved for use through 10/31/2002. OMB 0651-0032
Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
der the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Attorney Docket No. 30795-200443

PROVISIONAL APPLICATION FOR PATENT COVER SHEET

This is a request for filing a PROVISIONAL APPLICATION FOR PATENT under 37 CFR 1.53 (c).

	<u> </u>	IN'	VENTOR(S)						
Given Name (first and midd	le (if anyl)	Family N	lame or Surnam	e l	City and eithe	Residence			
Given Name (first and middle [if any])		Atherton		<u> </u>	(City and either State or Foreign Country) Leesburg, VA				
Peter S.		Atherion		- 1		Decision B.	;		
				ŀ					
Additional inventors are being named on the separately numbered sheets attached hereto									
			ENTION (280 ch	aracters	max)				
	A LOW COST	RADIO FR	EQUENCY IDE	NTIFICA	TION TAG				
	С	ORRESPO	NDENCE AD	DRESS		*266	94*		
Direct all correspondence to:					i	266			
Customer Number	26694	4			PATE		MARK OFFICE		
	ype Customer I	Vumber here	<u> </u>						
Firm or Individual Name	VENABLE LI	_P							
Address	P.O. Box 3438	P.O. Box 34385							
Address							·		
City	Washington		State	DC		ZIP	20043-9998		
Country	U.S.A.		Telephone	202.344		Fax	202.344.8300		
ENCLOSED APPLICATION PARTS (check all that apply)									
Specification Numb	er of Pages	8		CD(s), Number L				
☐ Drawing(s) Number of Sheets ☐ Other (specify)									
Application Data St	neet. See 37 0	FR 1.76							
METHOD OF PAYMENT OF	FILING FEES F	OR THIS PE	ROVISIONAL AF	PLICATI	ON FOR PAT	ENT (chec	k one)		
Applicant claims small		-							
							FILING FEE		
A check or money order is enclosed to cover the filing fees AMOUNT (\$)									
The Commissioner is hereby authorized to charge filing									
fees or credit any overpayment to Deposit Account Number: 22-0261 Payment by credit card. Form PTO-2038 is attached.									
The invention was made b	y an agency o	of the Unite	d States Gove	rnment	or under a co	ntract wi	th an agency of		
the United States Governr	nent.								
☐ No.			C		nwper ate.				
Yes, the name of the U.S.	Government a	gency and th	ne Government	Juliaci II		<u> </u>			
Respectfully submitted,	Will le	un) <i>,</i>	Date	1/22/0				
SIGNATURE			——— RE	GISTRA	TION NO.	42,709	9		
TYPED or PRINTED NAM	AE Jeffri A. Ka	minski		<i>ppropr</i> ia :ket Nuп	1	30795-200)443		
TELEPHONE 202-344-4	1800		500						

USE ONLY FOR FILING A PROVISIONAL APPLICATION FOR PATENT
This collection of information is required by 37 CFR 1.51, and is used by the public to file (and by the PTO to process) a provisional application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. SEND TO: Box Provisional Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



FEI	TRANSMITTAL	-
	for FY 2004	

Patent fees are subject to annual revision.

	Complete if Known	
Application Number		
Filing Date	January 22, 2004	
First Named Inventor	Peter S. Atherton	
Examiner Name		
Group / Art Unit		
Attorney Docket No.	30795-200443	

TOTAL AMOUNT OF PAYMENT (\$) 160					Attorn	Attorney Docket No. 30795-200443				/	
METHOD OF PAYMENT (check one)					FEE CALCULATION (continued)						
1. Deposit	The Commissi indicated fees				***	Fee	Large Entity Foe	Fee	Small Entity Fee	Fee Description	Fee
Account	22-0261			i		Code	(\$)	Code	(\$)		Pald
Number						1051 1052	130 50	2051 2052	65 25	Surcharge - late filing fee or oath Surcharge - late provisional filing fee or cover sheet.	
Deposit Account				ì		1053	130	1053	130	Non-English specification	
Name						1812	2,520	1812	2,520	For filing a request for reexamination	
	y Additional Fee CFR 1.16 and 1.1					1804	920*	1804	920°	Requesting publication of SIR prior to Examiner action	
See 37 C		y status.				1805	1,840*	1805	1,840*	Requesting publication of SIR after Examiner action	
2. ⊠ Payme	nt Enclosed:					1251	110	2215	55	Extension for reply within first month	
⊠ Check	☐ Credit card		ney 🗖 (Other		1252	420	2252	210	Extension for reply within second month	
	FEE C	ALCULATIO				1253	950	2253	475	Extension for reply within third month	
1. BASIC FI	LING FEE	ALCULATIO				1254	1,480	2254	740	Extension for repty within fourth month	
Large Entity	Small Entity					1255	2010	2255	1005	Extension for reply within fifth month	
Fee Fee	Fee Fee	Fee Descri	ption	M M - 1.4		1401	330	2401	165	Notice of Appeal	
Code (\$)	Code (\$)			Fee Pald	_	1402	330	2402	165	Filing a brief in support of an appeal	
1001 770	2001 385	Utility filing			4	1403	290	2403	145	Request for oral hearing	
1002 340 1003 530	2002 170 2003 265	Design filing			_	1451	1,510	1451	1,510	Petition to institute a public use proceeding	
1004 770	2004 385	Reissue fili	ng fee		_	1452	110	2452	55	Petition to revive - unavoidable	
1005 160	2005 80	Provisional	filling fee	160		1453	1,330	2453	665	Petition to revive - unintentional	
				(\$) 160	_	1501	1,330	2501	665	Utility issue fee (or reissue)	
Ì	SUBTOTA	AL (1)		(3) 100		1502	480	2502	240	Design Issue fee	
2. EXTRA CLA	M FEES					1503	64D	25403		Plant issue fee	
		Extra	Fee from	Fee		1460	130	1460	130	Petitions to the Commissioner	
Tatal Claims	—	Claims	x below	Paid Paid	\neg	1807	50	1807	50	Processing fee under 37 CFR 1.17 (q) Submission of Information Disclosure	\vdash
Total Claims Independent			î 🗀] = [₹	1608	180	1808	180	Stm1	
Ctaims Multiple		<u> </u>	x .] = 0	┪	8021	40	8021	40	Recording each patent assignment per property (times number of properties)	
Dependent Large Entit	y Small Enti	ity			_	1809	770	2809	385	Filing a submission after final rejection (37 CFR § 1.129(a))	
Fee Fee Code (\$)	Fee Fee Code (\$)	ree De	scription			1810	770	2810	385	For each additional invention to be examined (37 CFR § 1.129(b))	
1202 18	2202 9		n excess of 20			1801	770	2801	385	Request for Continued Examination (RCE)	1
1201 86	2201 43		dent claims in dependent da		id		900		900	Request for expedited examination	
1203 290 1204 86	2204 145 2204 43	** Reiss	ue independe			1602	900	. 1804	. 900	of a design application	
1		onginai	•	veers of 20 s	hoe	1					
1205 18	2205 9		ue daims in e ginal patent	ACCES OF ZO S	9110		.	:6.1			
			,		_	Other	fee (spec	··y)	_		لــــا
		SUBTOTAL	(2) (\$) 0			•Red	uced by B	asic Fill	ng Fee I	Paid SUBTOTAL (3) (\$) 0	
I	iously paid if great	or For Baiss	es sea shove			1					

		SUBMITTED BY Complete (# applicable)									
Name (Print/Type) Jeff	ri A. Kaminski	Registration No. Attorney/Agent)	42,709	Telephone	202-344-4800						
Signature	Adh 4	wir.		Date	1/22/04						

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. SEND TO: Commissioner for Patents, Mail Stop Patent Application, Alexandria, VA 22313-1450.

PC Docs No. 2/517202

Provisional Patent Specification for the Invention Entitled:

A Low Cost Radio Frequency Identification Tag

Inventor:

Peter Samuel Atherton

Address:

43811 Water Bay Terrace

Leesburg Virginia 20176 USA

This document contains 8 pages of text and 3 pages of figures.

Background to the Invention

Radio frequency identification (RFID) labels and tags are expected to enable the next generation of automated item identification technology. (In this document the terms "label" and "tag" are used interchangeably.) In particular it is expected that self-adhesive RFID labels and tags will be used extensively to tag items and containers.

In order for RFID tagging to be widely adopted it will need to be low-cost. The current conventional means of providing self-adhesive RFID tags involves producing discrete RFID tags that each includes all of the components needed to provide a complete RFID capability, and applying such tags to the items to be tagged. A disadvantage of this approach is that the production of complete, discrete RFID tags is intrinsically costly.

Disclosure of the Invention

The object of the current invention is to overcome or substantially ameliorate the above disadvantage.

There is disclosed herein a method for providing a low cost radio frequency identification (RFID) capability for an item, said method comprising:

- an item to be provided with an RFID capability;
- application of a radio frequency (RF) antenna directly to said item, preferably but not necessarily by printing said RF antenna on said item;
- an RFID electronics module that is provided separately from said item and said RF antenna;
- said RFID electronics module containing RFID electronics that provide an RFID capability when electrically coupled to said RF antenna;
- said RFID electronics module including a means to be applied to said item so as to be electrically coupled to said RF antenna on said item;
- application of said RFID electronics module to said item in a manner so as to couple said RFID electronics module to said RF antenna and thereby provide an RFID capability for said item.

Brief Description of the Figures

The principles of the present invention will now be described by way of non-limiting example with reference to the schematic illustrations in figures 1 to 3, wherein:

- Figures 1 and 2 are schematic illustrations of the essential features of a preferred embodiment of the current invention, showing an item with a pre-applied RF antenna and an RFID electronics module being applied to the item in the vicinity of said RF antenna so as to couple to said RF antenna and thereby provide a complete RFID function for said item;
- Figure 3 is a schematic illustration of one preferred embodiment of the RFID electronics module illustrated in figures 1 and 2.

Detailed Description of the Invention

In general an RFID tag provides the capability to store data electronically and to enable the stored data to be read from a distance by means of radio frequency (RF) techniques. In some cases an RFID tag may enable modification of said stored data.

An RFID tag consists of two distinct components:

- an RF antenna; and
- RFID electronics that are coupled to said RF antenna to provide an RFID capability.

In a conventional RFID tag both the RF antenna and the RFID electronics are integrated into the tag at the time of manufacture of the tag, so that the tags are produced as discrete, fully functional RFID devices that are applied to items to be tagged.

The basic concept underlying the present invention is that the RF antenna portion and the RFID electronics portion of an RFID tag are produced separately and assembled on the item to be tagged, the primary objective being to reduce the overall cost of the RFID tagging process (although other benefits do also accrue). Specifically, in the present invention the RF antenna is pre-applied to an item that is to be tagged and the RFID

electronics is applied separately to the item in the form of a discrete RFID electronics module that couples to the pre-applied RF antenna to provide an RFID capability for said item.

It should be appreciated that the term "item" as used herein is used in its broadest sense, and may for example refer to a product or product packaging.

The pre-applied RF antenna has no RFID capability in its own right, before the RF electronics module is applied.

Preferably, but not necessarily, the pre-applied RF antenna will be applied to an item by means of a printing process that may in one embodiment involve printing electrically conductive ink directly onto the surface of said item. Printing of said electrically conductive ink may be carried out in conjunction with printing of graphics, text, barcodes or other visible markings on said item.

It should be appreciated that in other embodiments the RF antenna may be made from materials other than electrically conductive inks. For example, in one embodiment the RF antenna may be made from a solid metal conductor or from a hybrid ink-plus-metal conductor.

Preferably, but not necessarily, the RFID electronics module will couple to the preapplied RF antenna by means of a non-contact coupling method such as capacitive coupling or inductive coupling.

Figures 1 and 2 are schematic illustrations of one embodiment of the present invention. In the embodiment of figures 1 and 2 an item 101 has an RF antenna 102 printed on it. An RFID electronics module 103 is subsequently applied to the item 101 in a specified position and orientation in the vicinity of the RF antenna 102 such that the RFID electronics in the module 103 couples to the RF antenna 102 to provide an RFID capability for the item 101. Figure 1 shows the RFID electronics module 103 before application to the item 101, while figure 2 shows the RFID electronics module 103 after

it has been applied to the item 101. In figures 1 and 2 the RFID electronics module 103 is shown as having a circular shape, but it should be appreciated that other shapes and configurations for the RFID electronics module 103 are possible, while still embodying the principles described herein for the present invention. Similarly, a specific RF antenna design 102 is illustrated in figures 1 and 2, but it should be appreciated that other RF antenna designs are possible, including induction loop designs for the RF antenna 102.

Preferably, but not necessarily, the RFID electronics module 103 will be applied to the item 101 by means of an adhesive layer on the RFID electronics module 103.

The RFID electronics in the RFID electronics module 103 may be either "passive" or "active". In this context the term "passive" means that the RFID electronics module 103 does not include a power source, while the term "active" means that the RFID electronics module 103 includes an on-board power source such as a battery.

In one preferred embodiment the RFID electronics module 103 is passive and the electronics in the module 103 comprises a single RFID integrated circuit (IC) connected to electrically conductive pads that enable non-contact coupling between the RFID electronics module 103 and the pre-printed antenna 102.

In the embodiment of figures 1 and 2 the RFID electronics module 103 preferably couples to the RF antenna 102 by means of a non-contact coupling method such as capacitive coupling or inductive coupling.

Figure 3 is a schematic illustration of one preferred embodiment of the RFID electronics module 103. In figure 3 the RFID electronics module 103 consists of a substrate 301 to which is attached an RFID IC 302. The RFID IC 302 is connected to electrically conductive pads 303 that enable non-contact coupling between the RFID electronics module 103 and the pre-printed antenna 102. The substrate 301, RFID IC 302 and electrically conductive pads 303 may be covered with a layer of adhesive that is used to attach the RFID electronics module 103 to the item 101. In one embodiment the substrate 301 may be a thin flexible substrate material, while in another embodiment

the substrate 301 may be a thicker material with recessed or contoured portions to house the RFID IC 302 and electrically conductive pads 303.

In a variation on the embodiment of the RFID electronics module 103 illustrated in figure 3, the RFID IC 302 may be designed to enable non-contact coupling to the RF antenna 102 without the need for electrically conductive pads 303, in which case the electrically conductive pads 303 will not be included in the RFID electronics module 103.

The use of non-contact coupling between the RFID electronics module 103 and the preprinted RF antenna 102 avoids the need to establish a direct electrical connection between the RFID electronics module 103 and the pre-printed RF antenna 102, thereby making assembly of the RFID electronics module 103 on the item 101 easier. In order to enable or optimize non-contact coupling it may be necessary to apply a layer of dielectric material between the RF antenna 102 and the RFID electronics module 103, for example by printing said dielectric material over the RF antenna 102. In those embodiments where the RFID electronics module 103 is applied to the item 101 by means of an adhesive layer said adhesive layer may provide a suitable dielectric layer between the RF antenna 102 and the RFID electronics module 103.

In some embodiments non-contact coupling between the RF antenna 102 and the RFID electronics module 103 may occur through a substrate material that is part of the item 101, so that the RF antenna 102 may be on one surface of a substrate material and the RFID electronics module 103 may be applied to the opposite surface of said substrate material. For example, the RF antenna 102 may be printed on the inside surface of a product package and the RFID electronics module 103 may be applied in a specified position and orientation to the outside surface of said product packaging such that the RF antenna 102 couples to the RFID electronics module 103.

It should be appreciated that in order for non-contact coupling between the RF antenna 102 and the RFID electronics module 103 to be effective it is necessary for the RFID electronics module 103 to be placed on the item 101 in a specified position and orientation relative to the RF antenna 102, within certain tolerances. Preferably, but not

necessarily, the non-contact coupling means may be designed so as to allow some misalignment of the RFID electronics module 103 and the RF antenna 102 while still providing effective non-contact coupling and an effective RFID capability. For example, in the case of capacitive coupling between electrical contact pads on the RF antenna 102 and electrical contact pads on the RFID electronics module 103, one set of contact pads – either on the RF antenna 102 or on the RFID electronics module 103 – may deliberately be made significantly larger than the other set and the contact pads may be spaced so as to allow a degree of misalignment of the RFID electronics module 103 relative to the RF antenna 102 while still providing effective capacitive coupling.

In one preferred embodiment the item 101 may include alignment marks to indicate where and how the RFID electronics module 103 should be placed to result in effective non-contact coupling to the RF antenna 102. In another preferred embodiment the item 101 may include surface features, such as a recessed area of specified size and shape, to aid in positioning of the RFID electronics module 103 on the item 101 and thereby produce effective non-contact coupling to the RF antenna 102. Similarly, the RFID electronics module 103 may include markings or colors to assist in applying the RFID electronics module 103 to the item 101 in the correct position and orientation so as to produce effective non-contact coupling between the RFID electronics module 103 and the RF antenna 102.

In some applications it may be important that the RFID electronics module 103 cannot be removed from an item 101 and reused on another item. Hence in some preferred embodiments the RFID electronics module 103 may be designed such that it will be damaged if it is removed after being applied to an item 101, thereby preventing the RFID electronics module 103 from being reused on another item. This self-destruct feature may result from (i) using a strong adhesive to attach the RFID electronics module 103 to the item 101; or (ii) including in the design of the RFID electronics module 103 certain weak points that are intended to break or separate or fail in some way if the RFID electronics module 103 is removed from the item 101; or (iii) other deliberately introduced design element(s) that result in damage to the RFID electronics module 103 if it is removed from the item 101.

In some applications it may be desirable for the RFID electronics module 103 to be easy to remove. For example, there are at present privacy concerns among some consumer groups that RFID may be used as a tracking mechanism after an item is purchased, so it may be desirable to provide consumers an easy way to disable the RFID capability on any tagged items that they purchase. In the context of the present invention this could be achieved by allowing easy removal of the RFID electronics module 103 from the item 101, and in some embodiments designing the RFID electronics module 103 to be damaged and therefore unusable after it has been removed from the item 101.

END

Inventor: Peter Atherton
Title of Invention: A Low Cost Radio
Frequency Identification Tag
Attorney Docket No.: 30795-200443
VENABLE

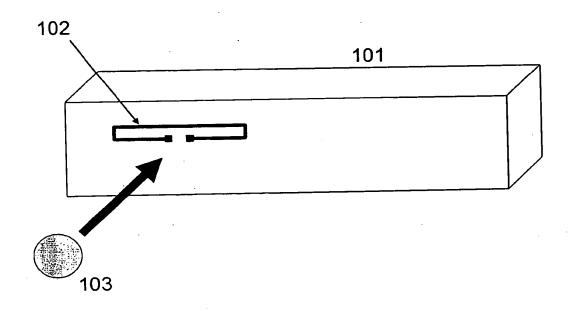


FIGURE 1

Inventor: Peter Atherton

Title of Invention: A Low Cost Radio Frequency Identification Tag Attorney Docket No.: 30795-200443 VENABLE

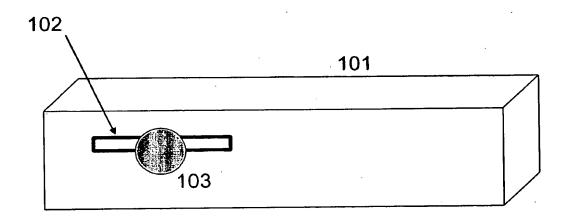


FIGURE 2

Inventor: Peter Atherton
Title of Invention: A Low Cost Radio
Frequency Identification Tag
Attorney Docket No.: 30795-200443
VENABLE

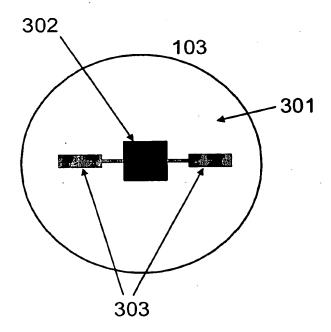


FIGURE 3